

ILLIXR Consortium

Democratizing XR research, development, and benchmarking



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illixr.org

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Founding consortium members: Arm, Meta Reality Labs, Micron, NVIDIA

Founding sponsor: ADA research center, a DARPA/SRC JUMP center



Extended Reality (XR): The Next Interface



Virtual, Augmented, Mixed Reality

The next computing interface

Will transform science, medicine, education, ...

BUT orders of magnitude gap
in power, performance, quality-of-experience
between current and desired systems

<i>Approximate</i>	Current	Desired
Res (Mpixels)	7	200
Power (W)	~7	0.1
Weight (g)	500	10
...

XR Systems: Challenges

Orders of magnitude gap

Power, performance, quality-of-experience (QoE)

<i>Approximate</i>	Current	Desired
Res (Mpixels)	7	200
Power (W)	~7	0.1
Weight (g)	500	10
...

Diverse expertise

graphics, vision, audio, video, optics, haptics, ...

Cross-layer system co-design

hardware, compiler, OS, algorithm

Complex metrics

multiple, user-driven, end-to-end QoE metrics

Closed systems, few participants

No open reference systems or benchmarks

Large barrier to entry for open R&D

How can we democratize XR systems research, development, benchmarking?

ILLIXR: Illinois Extended Reality Testbed

ILLIXR: Open-source full system XR testbed

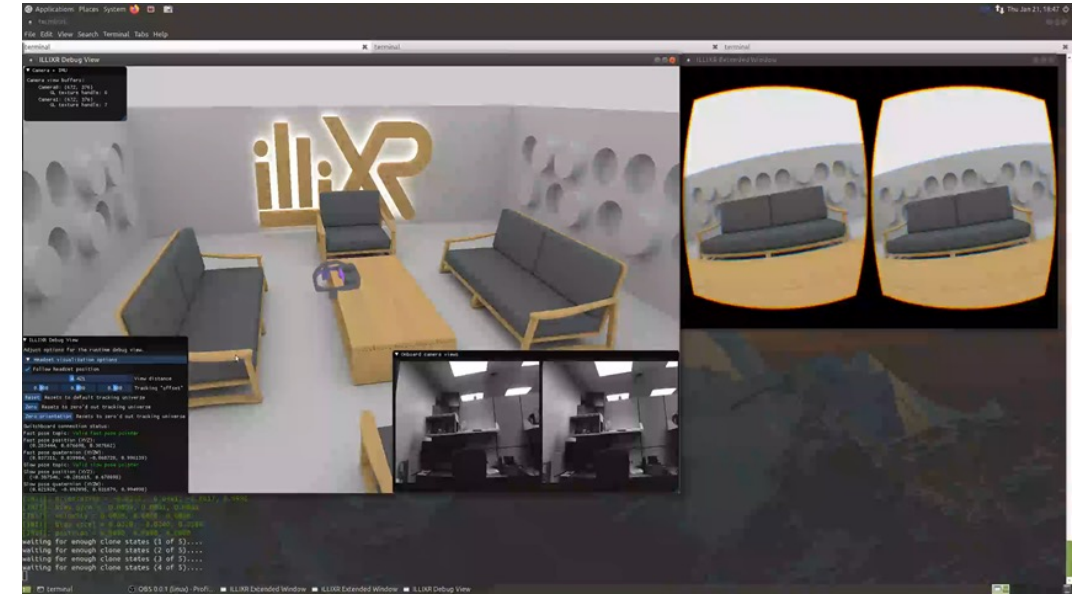
State-of-the-art XR components w/ modular runtime

OpenXR compatible

Extensive characterization and use for research

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Huzaifa et al., IISWC'21 best paper,
IEEE Micro Top Picks'22 (top 12 papers from 2021 architecture conferences)



SLiM Debug View

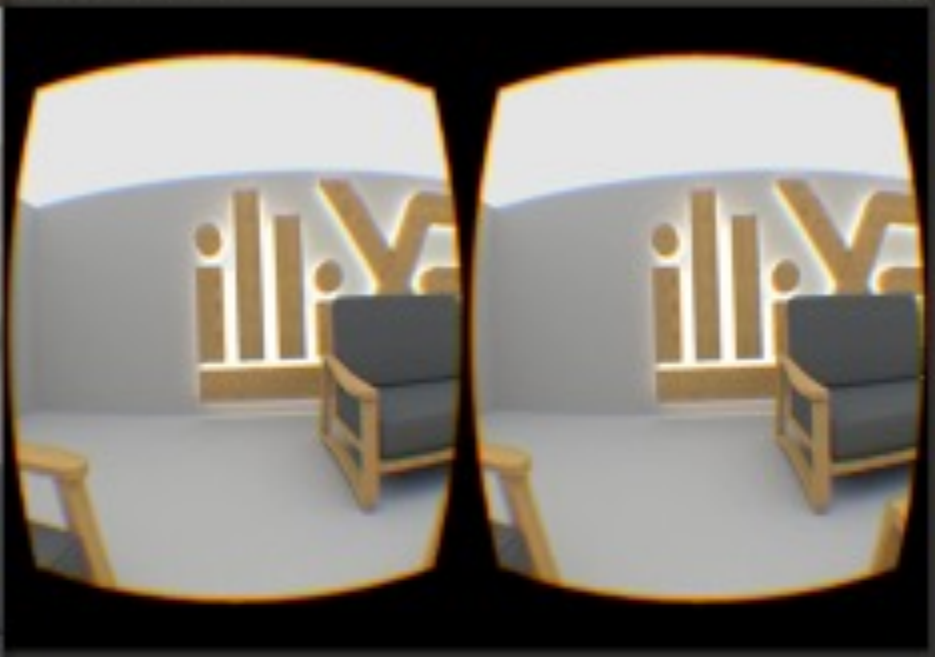
Camera: 360
 Camera view buffers:
 Camera: (ATI, 160)
 Camera: (ATI, 160)
 Camera: (ATI, 160)
 Camera: (ATI, 160)

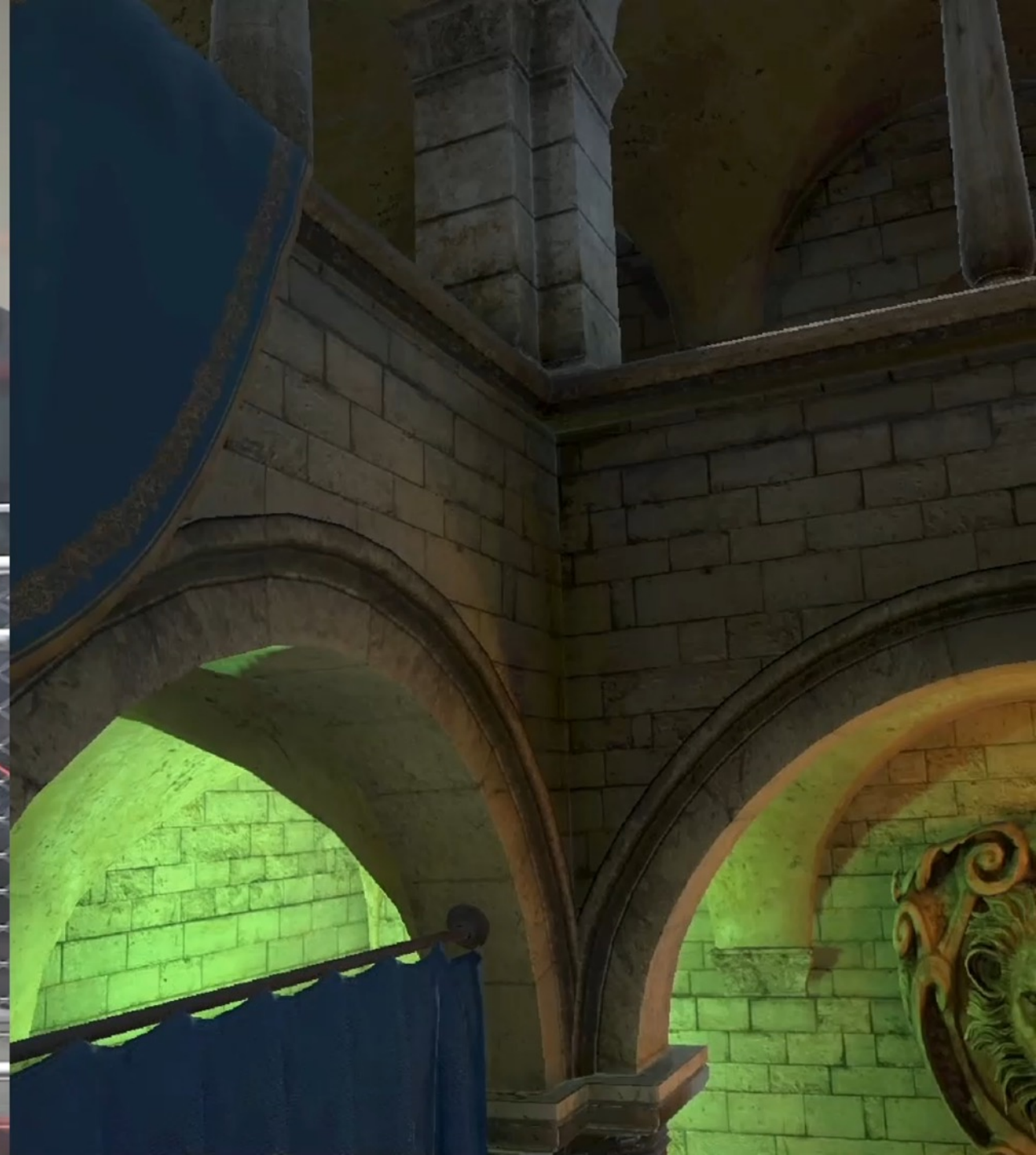


SLiM Debug View
 Adjust options for the running debug view.
 * Debug: visualization options
 * Follow: location position
 * [A, S, W] tracking "offset"
 * [WASD] tracks to default tracking camera
 * [QWE] tracks to zero'd out tracking camera
 * [ZXC] tracks to zero'd out tracking camera
 * [VBN] tracks to zero'd out tracking camera
 Set address/connection status:
 * [P] port open: [0] to [255] port open
 * [N] port open: [0] to [255]
 * [M] port open: [0] to [255], [0] to [255]
 * [L] port open: [0] to [255], [0] to [255], [0] to [255]
 * [K] port open: [0] to [255], [0] to [255], [0] to [255]
 * [J] port open: [0] to [255]
 * [I] port open: [0] to [255], [0] to [255]
 * [H] port open: [0] to [255], [0] to [255], [0] to [255]



SLiM Embedded Window







ILLIXR Consortium

ILLIXR Consortium w/ industry + academic partners

- Arm, Meta, Micron, North Star, NVIDIA, ...

Goals

- Reference open-source testbed
 - Components and interfaces
 - Modular, extensible runtime
 - Telemetry
- Benchmarking methodology
 - Applications, data sets
 - System configurations
 - Metrics
- Build XR systems research and development community

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Home About Testbed Working Groups Get Involved Discord

illixr

We aim to democratize extended reality (XR) systems research, development, and benchmarking by providing

A reference open source end-to-end XR system testbed, with state-of-the-art components connected with a modular runtime

A benchmarking methodology for XR systems, including reference system configurations, applications, data sets, and metrics

A community where the multidisciplinary XR systems research and development stakeholders come together

[Read more](#)

ada Muhammad Huzaifa - ILLIXR: Ill...

Now funded by NSF CISE community research infrastructure program

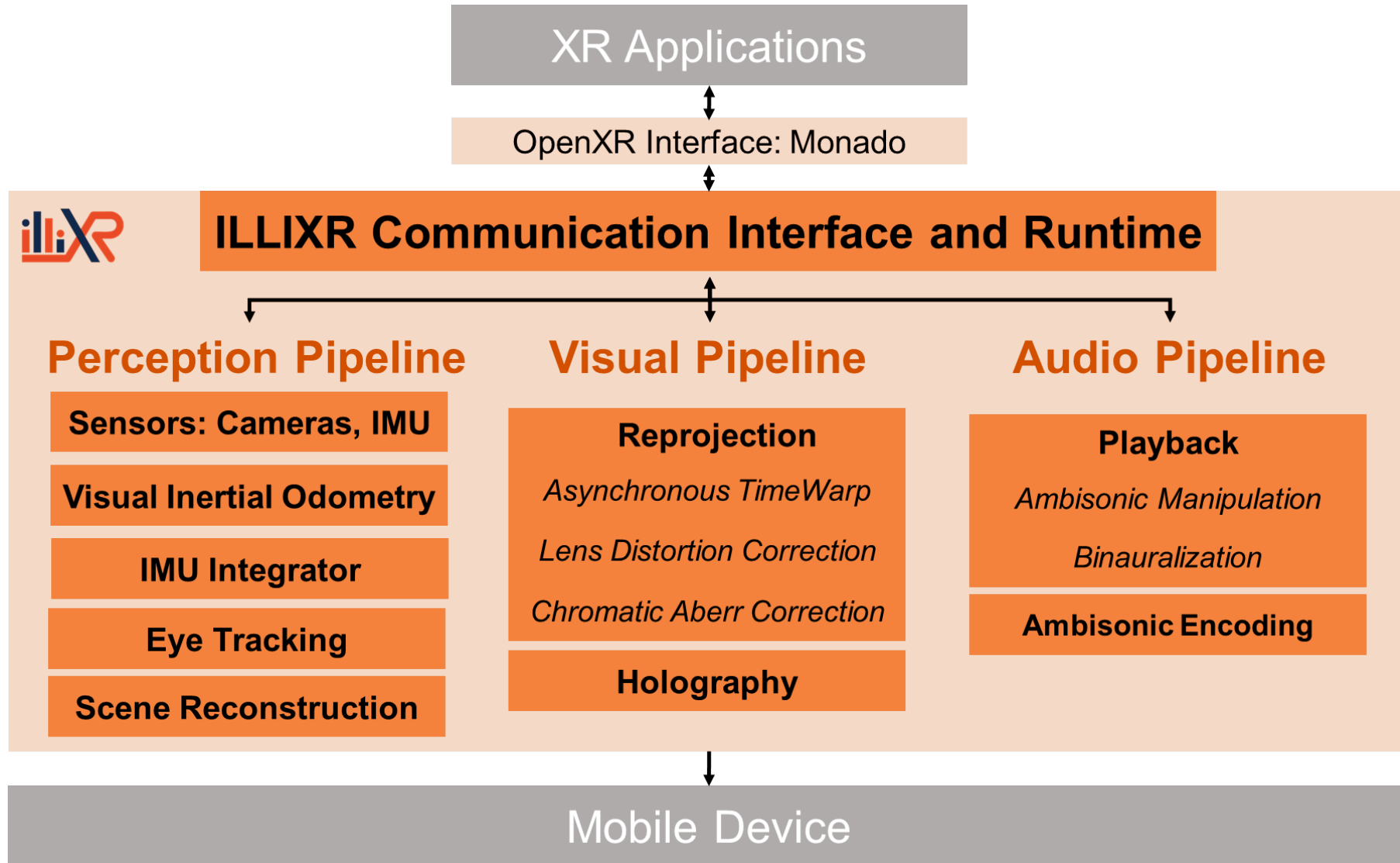
Join us: illixr@cs.illinois.edu, illixr.org, discord, weekly meetings



Outline

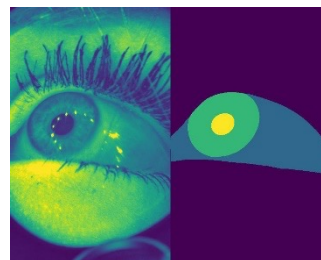
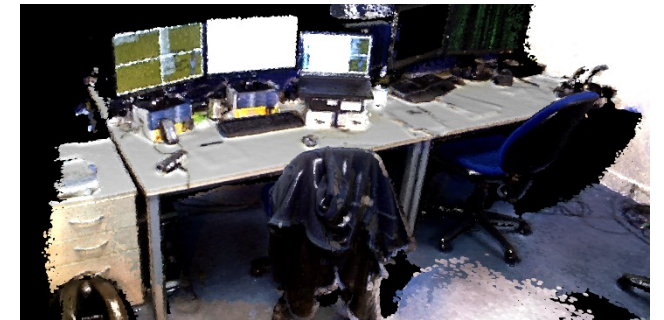
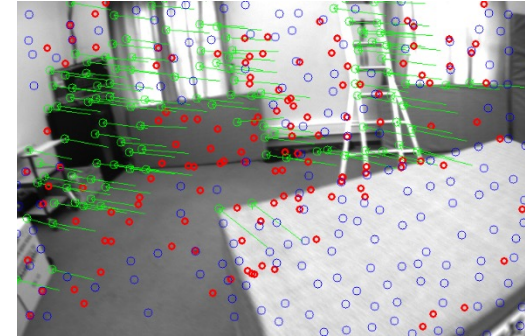
- Introduction
- **ILLIXR Description**
- Evaluation and Implications
- Ongoing Research with ILLIXR
- Ongoing Work on ILLIXR Testbed
- Consortium Organization and Goals

ILLIXR Overview



Perception Pipeline

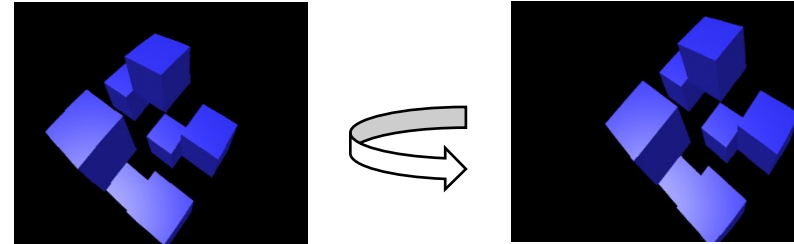
- Sensors: Camera, Inertial Measurement Unit (IMU)
- Visual Inertial Odometry (VIO)
 - Provides position and head orientation (pose)
- IMU Integrator
 - Provides high frequency pose estimates
- Pose Predictor
 - Extrapolates pose to future timestamp
- Scene Reconstruction
 - Uses RGB-Depth camera to build dense 3D map of world
- Eye Tracking



Visual Pipeline

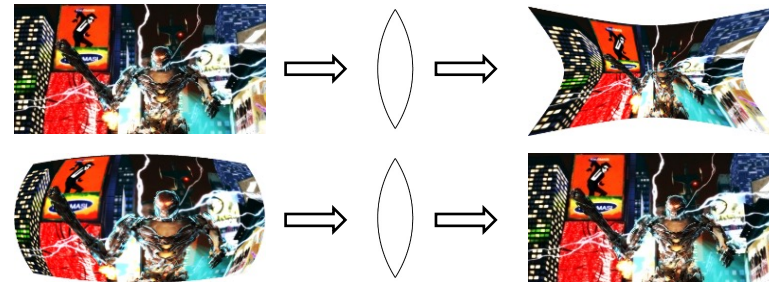
- Asynchronous reprojection

- Warp rendered frame to account for head movement during rendering
- Uses latest pose estimate and prediction
- Cuts motion-to-photon latency



- Lens distortion and chromatic aberration correction

- Corrects for distortion due to curved lenses



- Computational holography

- Vergence-accommodation conflict (VAC): eyes focused at fixed point, converge at different points
- Computational displays w/ multiple focal planes can fix VAC: compute per-pixel phase shift

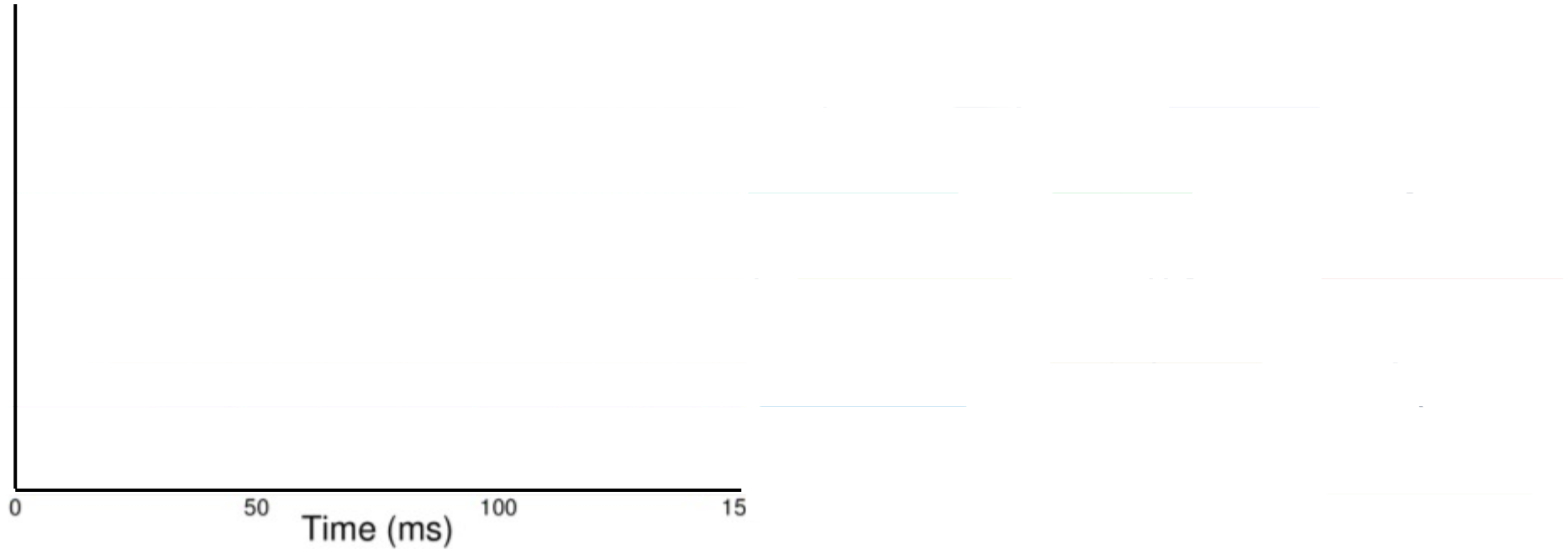
Audio Pipeline

- Audio encoding
 - Encodes multiple sound sources into Higher Order Ambisonics (HOA) soundfield
- Playback
 - Rotates and zooms HOA sound field for user's latest pose
 - Performs binauralization to account for user's ear, head, nose

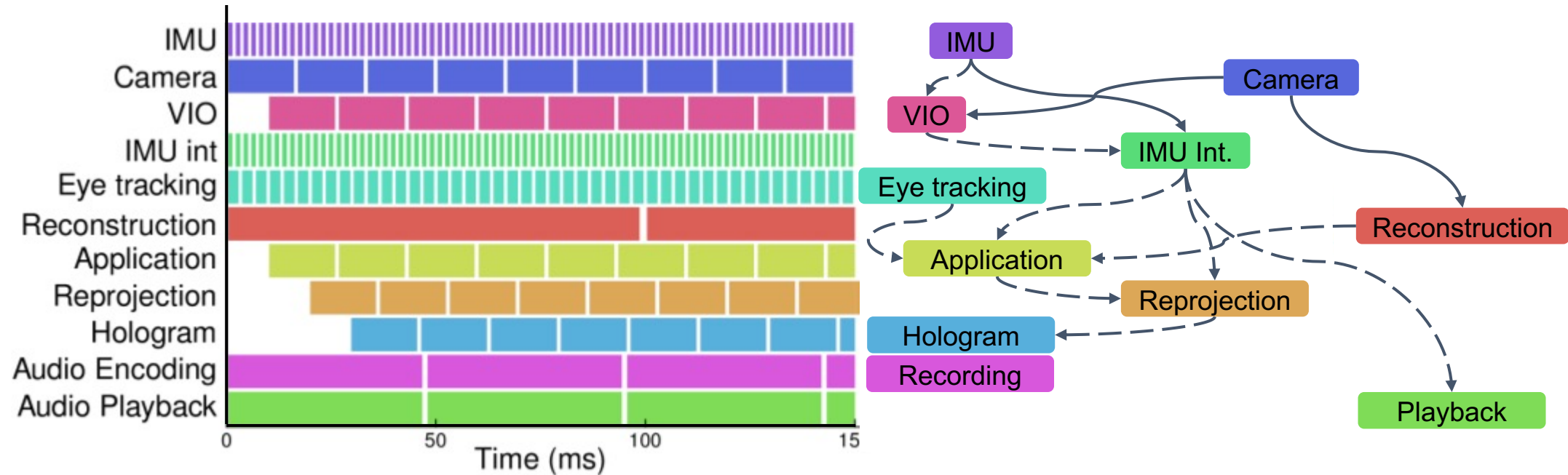
BUT XR is not just a collection of components

It is a SYSTEM

XR System Dataflow

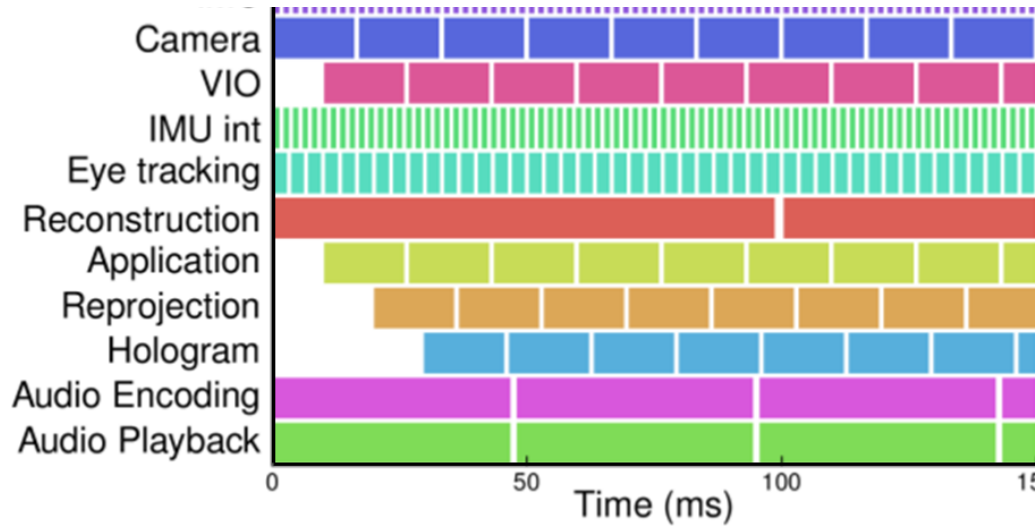


XR System Dataflow



Different components at different frequencies
Multiple interacting pipelines
Synchronous and asynchronous dependences
Multiple quality of experience metrics

ILLIXR Runtime



Modular, flexible architecture

ILLIXR components are plugins

Separately compiled, dynamically loaded

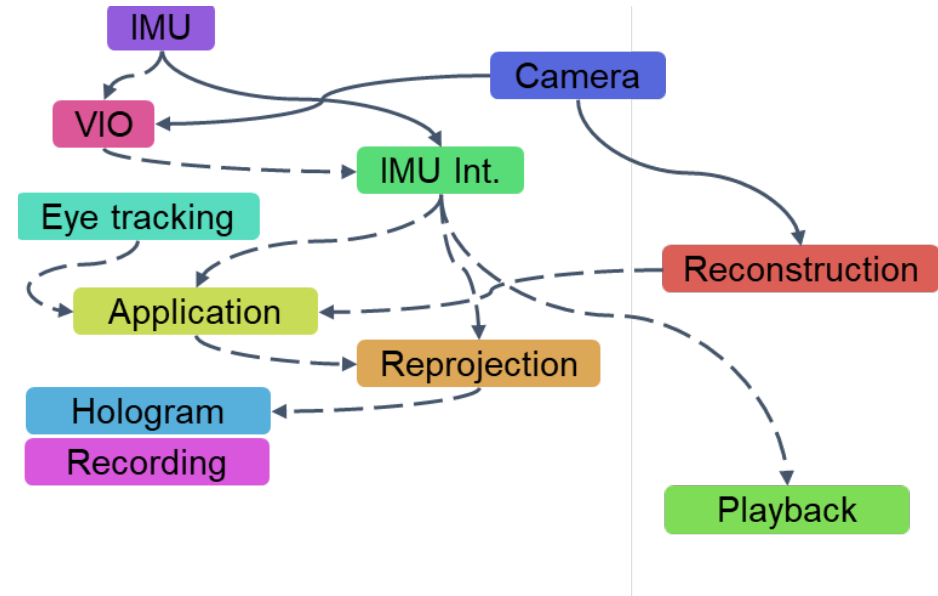
Easily swap/add new components, implementations

Efficient, flexible communication interface

Component specifies event streams to publish, subscribe

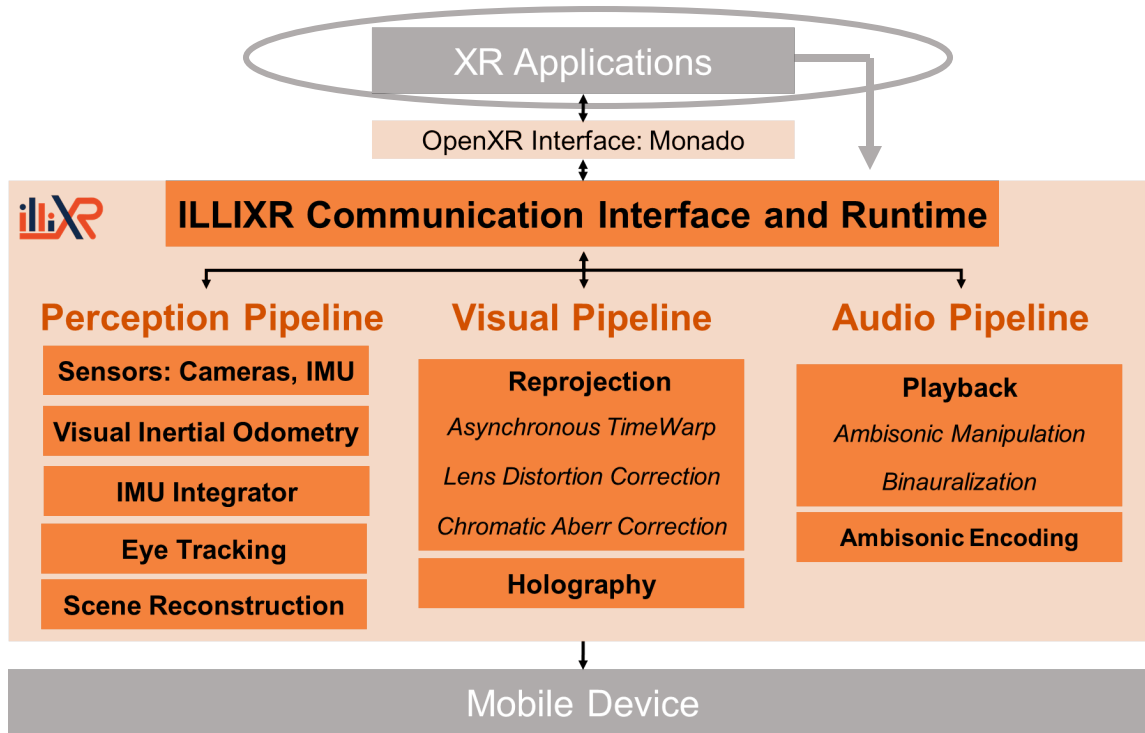
Synchronous or asynchronous consumers

Copy-free, shared memory implementation



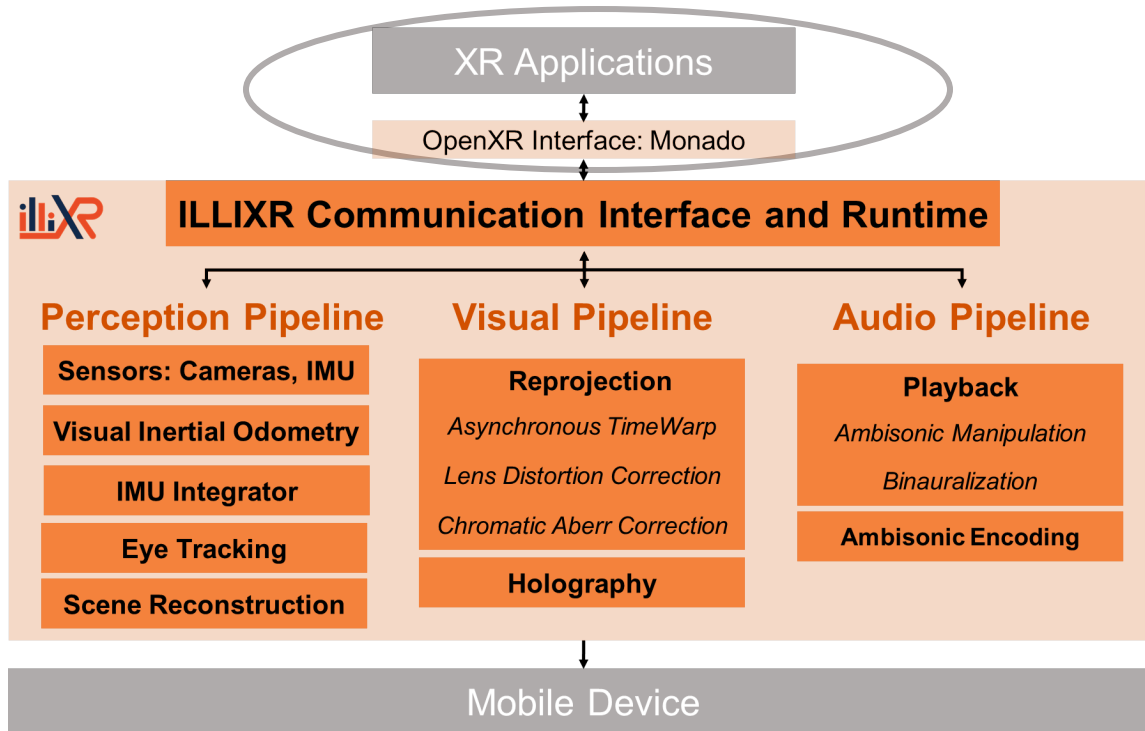
End-to-end system balances flexibility with efficiency

ILLIXR Applications



Can write XR applications directly to ILLIXR

ILLIXR Applications



Can write XR applications directly to ILLIXR

ILLIXR supports OpenXR applications

- Uses Monado implementation of OpenXR
- Today: Godot game engine with many apps
- Soon: Unity, Unreal, ...

End-to-End Quality Metrics

- Motion-to-photon latency
 - Time from head motion to display (currently w/o display latency)
- Image quality: SSIM and FLIP
- + Extensive telemetry: Frame rates, missed frames, time distributions, power, ...

ILLIXR Components and Systems Today

	Component	Algorithm	Implementation
Perception Pipeline	Camera	ZED SDK	C++
	Camera	Intel RealSense SDK	C++
	IMU	ZED SDK	C++
	IMU	Intel RealSense SDK	C++
	VIO	OpenVINS	C++
	VIO	Kimera-VIO	C++
	IMU Integrator	RK4	C++
	IMU Integrator	GTSAM	C++
	Eye Tracking	RITnet	Python, CUDA
	Scene Reconstruction	ElasticFusion	C++, CUDA, GLSL
Scene Reconstruction	KinectFusion	C++, CUDA	
Visual Pipeline	Reprojection	VP-matrix reproject w/ pose	C++, GLSL
	Lens Distortion	Mesh-based radial distortion	C++, GLSL
	Chromatic Aberration	Mesh-based radial distortion	C++, GLSL
	Adaptive Display	Weighted Gerchberg-Saxton	CUDA
Audio Pipeline	Audio Encoding	Ambisonic encoding	C++
	Audio Playback	Ambisonic manipulation, binauralization	C++

Systems
Desktop PC
Jetson Xavier

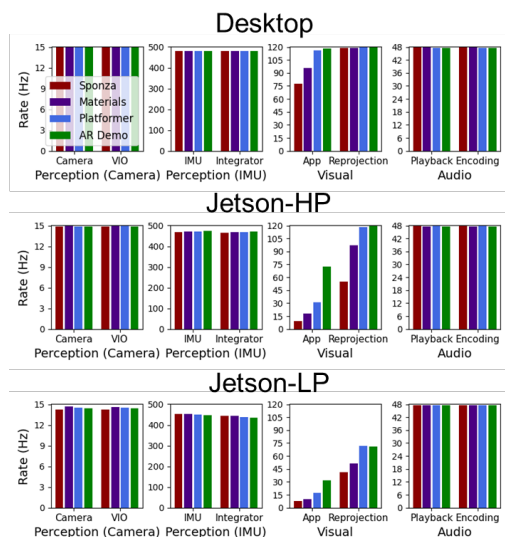


Outline

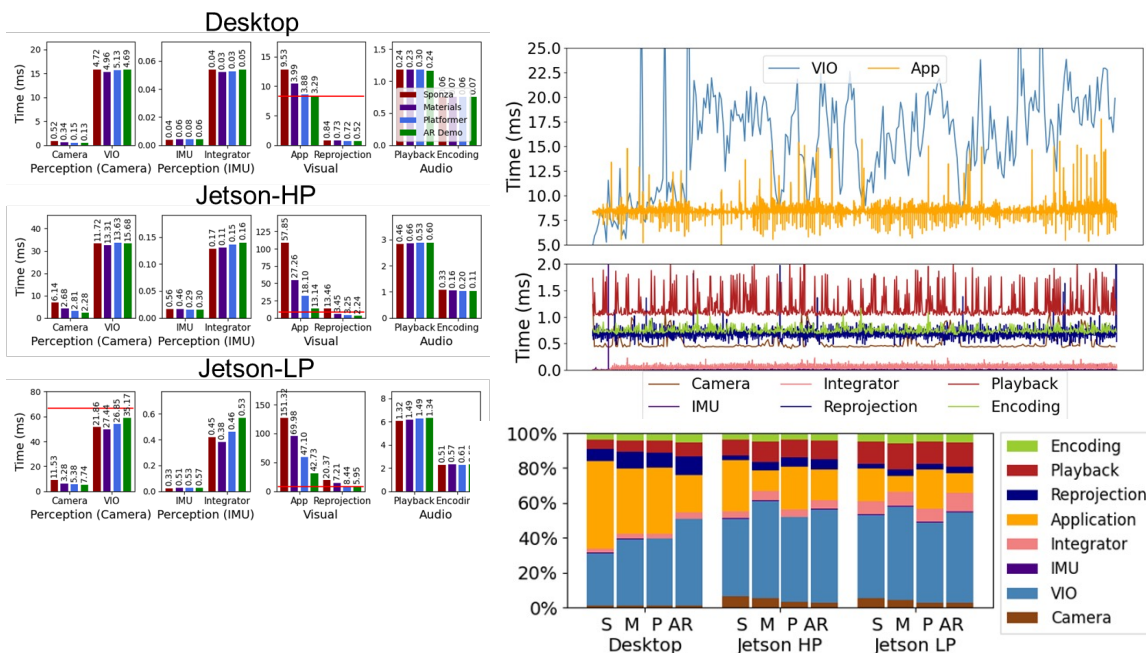
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Results Summary

Frame Rate

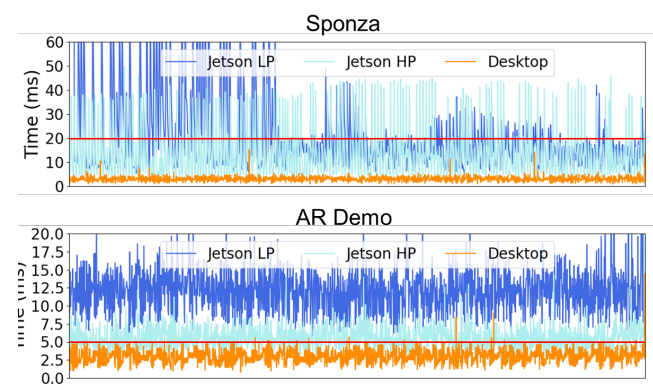


Execution Time & Distribution

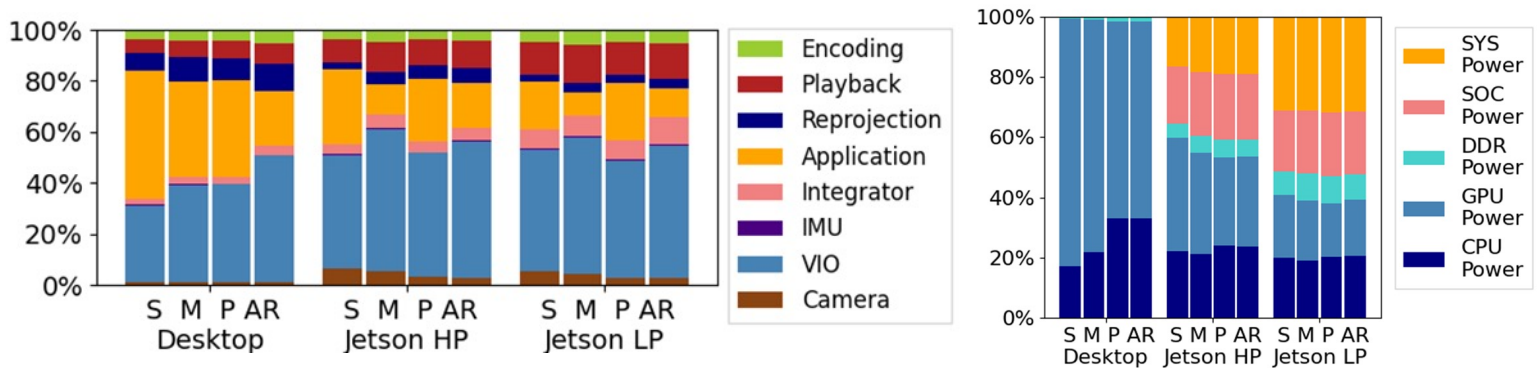


Quality of Experience

Application	Desktop	Jetson-HP	Jetson-LP
Sponza	3.1 ± 1.1	13.5 ± 10.7	19.3 ± 14.5
Materials	3.1 ± 1.0	7.7 ± 2.7	16.4 ± 4.9
Platformer	3.0 ± 0.9	6.0 ± 1.9	11.3 ± 4.7
AR Demo	3.0 ± 0.9	5.6 ± 1.4	12.0 ± 3.4



Power

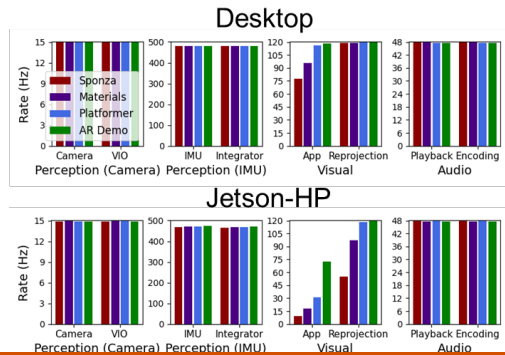


Platform	SSIM	1-FLIP
Desktop	0.83 ± 0.04	0.86 ± 0.05
Jetson-HP	0.80 ± 0.05	0.85 ± 0.05
Jetson-LP	0.68 ± 0.09	0.65 ± 0.17

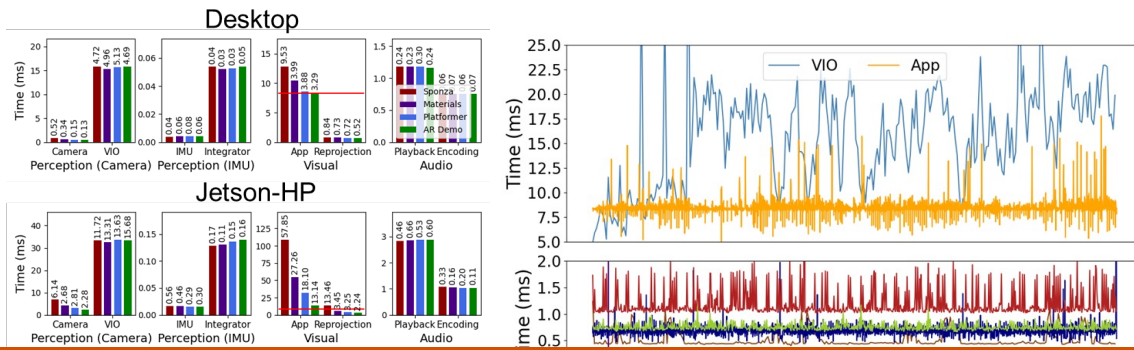


Results Summary

Frame Rate



Execution Time & Distribution

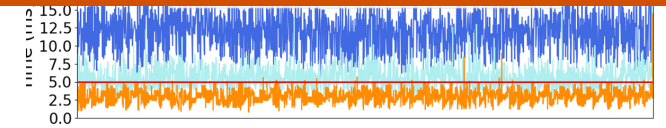
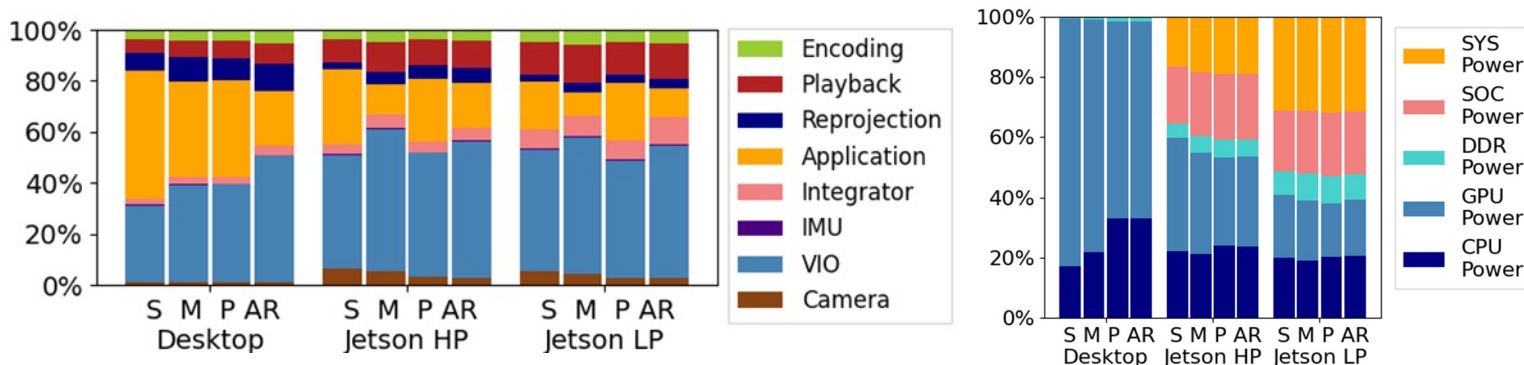


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First published performance/power/QoE results for end-to-end XR system

Power



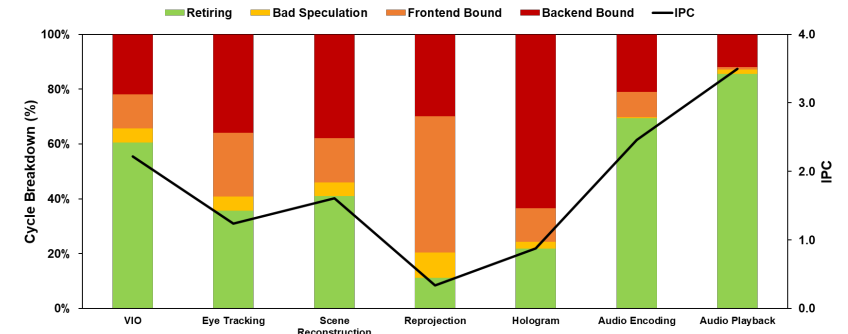
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Summary of Implications for System Designers

Standalone Components

- Need to specialize hardware, software, *system*
- Must consider all application components in *system* together
- Must consider *system*-level hardware components; e.g., display and I/O
- Need to partition, allocate, and schedule *system* resources
- Must look at entire *system* to make QoE-driven tradeoffs
- Abundance of tasks and no single task dominates



⇒ Need *automated* techniques to determine what to accelerate

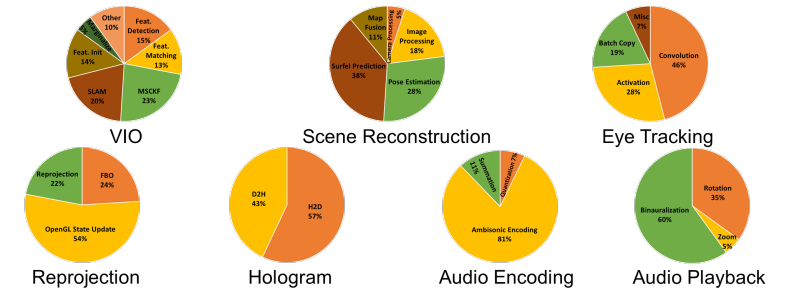
- Impractical to build accelerator for every task

⇒ Must build *shared* hardware

- Diversity of compute and memory primitives

⇒ *Flexible* on-chip memory hierarchy

⇒ *Flexible* accelerator communication interface



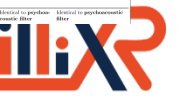
- Algorithms in flux

⇒ Must design *programmable* hardware

- Different algorithms have different QoE vs. resource usage profiles

⇒ End-to-end QoE driven *approximate* computing

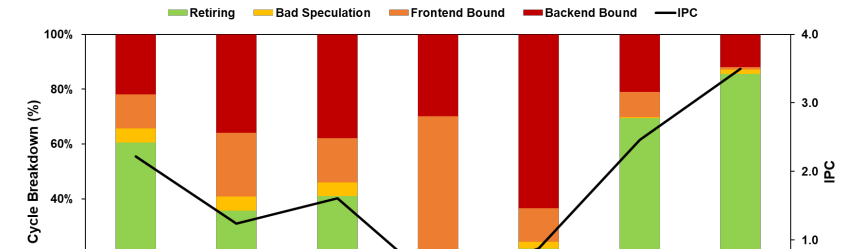
Task	Time	Computation	Memory Pattern	
Feature detection	15%	Integer straddles per each pixel	Locally dense straddles; globally mixed dense and sparse	
Feature matching	14%	Integer arithmetic; GEMM; linear algebra	Locally dense; globally mixed dense and sparse; mixed dense and sparse; feature map access	
Filters	62%	Gaussian; Newton refinement; QR decomposition; GEMM; linear algebra	Strided dense and sparse feature map and filter matrix accesses	
Other	10%	Gaussian filter; histogram	Globally dense straddles	
Miscellaneous tasks	24%	Handcrafted kernel and clear		
Task	Time	Computation	Memory Pattern	
FB	24%	Handcrafted kernel and clear	Driver-side CPU-GPU communication	
FB state management				
OpenGL State Update (out-of-OpenGL state)	54%	OpenGL state updates; one general per eye	Driver-side CPU-GPU communication	
Reprojection	22%	6 matrix-vector multiplies	Accesses uniform, vertex, and fragment buffers; 3 texture accesses/fragment	
Task	Time	Computation	Memory Pattern	
Hologram-to-depth	57%	Transcendentals; FMADDs; FB-wide tree reduction	Dense row-major; spatial locality in pixel data; temporal locality in depth data; reduction in scratchpad	
Propagates pixel plane to depth plane				
Sum	<	Tree reduction	Dense row-major; reduction in scratchpad	
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Depth-to-hologram	43%	Transcendentals; FMADDs; threshold reduction	Dense row-major; no pixel reads; pixels written once	
Propagates depth plane to pixel				
Task	Time	Computation	Memory Pattern	
Canvas Processing	5%	Bilateral filter; local depth operation	Dense sequential accesses to depth image	
Image Processing	18%	Generation of vertex maps; normal maps, and image statistics; image sub-sampling; post-transformation of old map	Globally dense; local straddles; change from RGB:RGB → RGB:G:ID	
Pose Estimation	28%	RCP; photometric error; geometric error	Photometric error is globally dense; others are globally sparse; locally dense	
Surfel Prediction	30%	Vertex and fragment shaders	Globally sparse; locally dense	
Map Fusion	11%	Vertex and fragment shaders	Globally sparse; locally dense	
Task	Time	Computation	Memory Pattern	
Normalization (INT16 to FP32)	7%	Element-wise FP32 division	Dense column-major	
Encoding	81%	$Y'_{ij}[t] = D \times X_{ij}[t]$	Dense column-major	
Summation	11%	$Y'_{ij}[t] += X_{ij}[t] \times W$	Dense row-major	
Task	Time	Sub-task	Computation	Memory Pattern
Render	40%	Depth	FP32; branching; dot	Global access for many operations; FP32; dot; sequential access for
Surfel Prediction	30%	Image	Texture reads; texture writes	Dense; dense; dense
Map Fusion	11%	Image	Texture reads; texture writes	Dense; dense; dense
Other	10%	Image	Texture reads; texture writes	Dense; dense; dense
Task	Time	Computation	Memory Pattern	
FB	24%	Handcrafted kernel and clear	Driver-side CPU-GPU communication	
FB state management				
OpenGL State Update (out-of-OpenGL state)	54%	OpenGL state updates; one general per eye	Driver-side CPU-GPU communication	
Reprojection	22%	6 matrix-vector multiplies	Accesses uniform, vertex, and fragment buffers; 3 texture accesses/fragment	
Task	Time	Computation	Memory Pattern	
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Propagates depth plane to pixel				



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- Must look at entire *system* to make QoE-driven tradeoffs

Standalone Components



ILLIXR = Rich playground for systems research

Enables QoE-driven, end-to-end hardware-software-algorithm codesigned systems research

- Diversity of compute and memory primitives
 - ⇒ *Flexible* on-chip memory hierarchy
 - ⇒ *Flexible* accelerator communication interface
- Algorithms in flux
 - ⇒ *Must design programmable hardware*
- Different algorithms have different QoE vs. resource usage profiles
 - ⇒ *End-to-end QoE driven approximate computing*



Task	Time	Computation	Memory Pattern	
Feature detection	15%	Larger stencil per each pyramid level	Locally dense stencil; globally tiled dense and sparse	
Feature matching	14%	Integer arithmetic; GEMM; linear algebra	Locally dense stencil; globally tiled dense and sparse; tiled dense and sparse; feature map access	
Filter	62%	Gauss-Newton refinement; QR decomposition; GEMM; linear algebra	Strided dense and sparse feature map and filter matrix access	
Other	-	-	-	
Miscellaneous tasks	10%	Gaussian filter; histogram	Globally dense stencil	
Task	Time	Computation	Memory Pattern	
FB	24%	Handcrafted loop and chain	Driver-side CPU-GPU communication	
FB state management	-	-	-	
OpenCL State Update (out-of-OpenCL state)	54%	OpenCL state updates; one shared per eye	Driver-side CPU-GPU communication	
Reprojection	22%	6 matrix-vector MVA/vectors	Accesses uniform, vertex, and fragment buffers; 3 vector accesses/fragment	
Task	Time	Computation	Memory Pattern	
Hologram-to-depth	57%	Transcendentals; FMADDS; TB-wide tree reduction	Dense row-major; spatial locality in pixel data; temporal locality in depth data; reduction in scratchpad	
Sum Sums phase differences (from hologram-to-depth)	<	Tree reduction	Dense row-major; reduction in scratchpad	
Depth-to-hologram	43%	Transcendentals; FMADDS; threshold-reduction	Dense row-major; no pixel reads; pixels written once	
Propagates depth plane phase to pixel	-	-	-	
Task	Time	Sub-task	Computation	Memory Pattern
Camera Processing	95%	Pre-process incoming camera depth image	Bilateral filter; localized depth operation	Dense sequential access to depth image
Image Processing	18%	Pre-process RGB-D image for tracking and mapping	Generation of vertex maps, normal maps, and image statistics; image sub-sampling; post-transformation of all maps	Globally dense local stencil; layered change from RGB-D to RGB-LID
Pose Estimation	28%	Estimates RDOF pose	RCP; photometric error; geometric error	Photometric error is globally dense; others are globally sparse, locally dense
Surface Prediction	38%	Calculate active voxels in current frame	Vertex and fragment shaders	Globally sparse; locally dense
Map Update	11%	Updates map with new surface information	Vertex and fragment shaders	Globally sparse; locally dense
Task	Time	Computation	Memory Pattern	
Normalization (INT16 to FP32)	7%	Element-wise FP32 division	Dense row-major	
Encoding	87%	Sample to soundfield mapping	$Y_{ij}^{(k)} = D \times X_{ij}^{(k)}$	Dense column-major
Summation	-	BINA soundfield summation	$Y_{ij}^{(k)} += X_{ij}^{(l)} \forall k$	Dense row-major
Task	Time	Sub-task	Computation	Memory Pattern
Render	95%	Pre-render scene	RTV; geometry; color; material; texture; depth; opacity	Global access for material; dense access for geometry
Scene	5%	Scene volume map	Transcendentals; FMADDS; threshold-reduction	Dense row-major; no pixel reads; pixels written once
Depth	5%	Depth plane phase to pixel	Linear algebra	Dense column-major sequential access
Blending	5%	Blending operation	Blending operation	Blending operation



Ongoing Research with ILLIXR

- Representing heterogeneous parallelism in software
- Automated selection and generation of accelerator hardware and software
- Accelerator communication interfaces
- Automated approximation selection
- Cross-component co-design
- QoE-driven scheduling
- Computation offload
- QoE metrics
- XR algorithms
- Eye tracking + Holograms [Sivasubramaniam et al., Micro'21]

Ongoing Work on Testbed

- New components: spatial reprojection, hand tracking, foveated rendering, ...
- Off-loading, streaming, multiparty XR (ILLIXR + CONIX ARENA)
- Broaden hardware/software platforms supported: Dockerization, Android, ...
- Create and curate datasets and applications
- Incorporate research results

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Consortium Goals

- Reference open-source testbed
 - Components and interfaces
 - Modular, extensible runtime
 - Telemetry
- Benchmarking methodology
 - Applications, datasets
 - System configurations
 - Metrics
- Build XR systems research and development community

Weekly open meetings and working groups on various topics

Executive Committee

Responsibilities

- Oversight of overall direction of the project
- Executive committee membership
- Approving working groups and their chairs
- Approving working group recommendations and outcomes
- Setting project policies, including governance and membership agreements
- Outreach for the project

Current members

- Sarita Adve, University of Illinois, Chair
- Muhammad Huzaifa, University of Illinois, Vice-Chair
- Ameen Akel, Micron
- Sean Eilert, Micron
- Art Goldberg, Arm
- Rod Hooker, Meta
- Matt Horsnell, Arm
- Vasileios Laganakos, Arm
- David Luebke, Nvidia
- Maurizio Paganini, Meta
- Anthony Rowe, CMU

Advisory Board

Current members

- Ameen Akel
- Sarita Adve
- Vikram Adve
- Valeria Bertacco
- David Brooks
- Rod Hooker
- Matt Horsnell
- Muhammad Huzaifa
- Steve LaValle
- David Luebke
- Sharad Malik
- Franziska Roesner
- Anthony Rowe
- Gu-Yeon Wei

Providing overall strategic vision and direction of the consortium

Working Groups

- Any representative of a consortium member organization may attend working group meetings
- Consortium members have voting privileges in the working groups (one vote per organization)
- Only consortium member representatives may chair a working group
- Participants from non-member organizations are welcome to join a working group, subject to lightweight approval and without voting or chair privileges

Planned Working Groups

- Metrics
- Interfaces
- Components
- Computation offload
- Distributed XR
- Configurations and benchmark reporting
- Applications
- Datasets
- Infrastructure
- Research related

Next week

- Intro to ARENA (distributed XR), integrating w/ ILLIXR

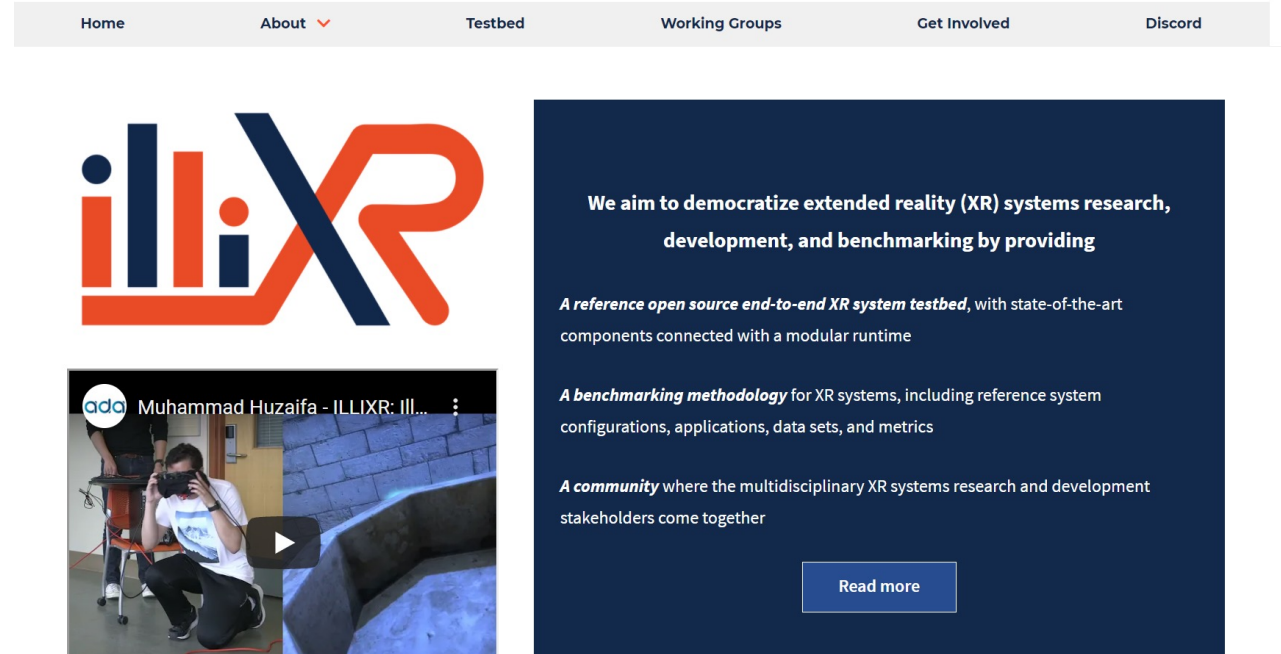
Anthony Rowe, CMU

ILLIXR Consortium

Goals

- Reference open-source testbed
- Benchmarking methodology
- Build XR systems R&D community

illixr.org



The screenshot shows the homepage of the illixr.org website. At the top, there is a navigation bar with links for Home, About (with a dropdown arrow), Testbed, Working Groups, Get Involved, and Discord. Below the navigation bar is a large blue banner. On the left side of the banner is the illixr logo, which consists of the letters 'i', 'l', 'i', 'x', and 'r' in a stylized, colorful font. To the right of the logo, the text reads: "We aim to democratize extended reality (XR) systems research, development, and benchmarking by providing". Below this, there are three bullet points: "A reference open source end-to-end XR system testbed, with state-of-the-art components connected with a modular runtime", "A benchmarking methodology for XR systems, including reference system configurations, applications, data sets, and metrics", and "A community where the multidisciplinary XR systems research and development stakeholders come together". At the bottom right of the banner is a "Read more" button. Below the banner is a video player showing a person using a VR headset, with the title "Muhammad Huzaifa - ILLIXR: Ill..." and a play button icon.

Join us: illixr@cs.illinois.edu, illixr.org, [discord](#), [weekly meetings](#)

