

ECE 546 SPRING 2024

Instructor

José Schutt-Ainé - 5042 ECEB (jesa@illinois.edu)

Class Time

1 -1:50 pm, MWF, 2013 ECEB

Textbooks - Required

S. Hall and H. Heck, Advanced Signal Integrity for High-Speed Digital Designs. IEEE-J.Wiley, 2009

Textbooks - Recommended

1. W. J. Dally and J. W. Poulton, "Digital Systems Engineering", Cambridge University Press, 1998
2. Kyung Suk (Dan) Oh and Xingchao (Chuck) Yuan, High-Speed Signaling: Jitter Modeling, Analysis, and Budgeting, Prentice Hall, 2012.
3. Madhavan Swaminathan and Ege Engin, Power Integrity Modeling and Design for Semiconductor and Systems, Prentice Hall, 2007.
3. Brian Young, Digital Signal Integrity, Prentice Hall, 2001.

Grading Policy

Homework	60% of total
Project	30% of total
Participation	10% of total

Homework Policy

Problems are assigned every week and are due the following week at the end of the class period. Homework is to be the student's own work, not a collaborative or plagiarized work. However, students are permitted and encouraged to help one another by engaging in discussion of course material and approaches to solving the homework problems. Homework solutions will be posted on the course web page.

Teaching Assistant

Yi Zhou (yizhou18@illinois.edu)

WWW Home Page

The course internet home page can be found at <http://emlab.illinois.edu/ece546>

Office Hours

José Schutt-Ainé - Wednesday 3-4 pm